



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: CQ10177

Kuei-Ann WEN

Appln. No.: 10/678,375

Group Art Unit: 2825

Confirmation No.: 4031

Examiner: Yelena ROSSOSHEK

Filed: October 3, 2003

For: MODULARIZED CIRCUIT DESIGN INFORMATION GENERATING METHOD,  
MODULARIZED CIRCUIT DESIGN INFORMATION GENERATING TOOL AND  
INTEGRATED CIRCUIT PREPARED WITH CIRCUIT DESIGN INFORMATION  
GENERATED THEREFROM

**AMENDMENT UNDER 37 C.F.R. § 1.111**

**MAIL STOP AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated November 15, 2005, please amend the above-identified application as follows on the accompanying pages.

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